

Patent title:	Method for making the comparison between a continuous value analogue signal and a continuous value digital signal and circuit architecture configured to carry out the aforesaid method
FBK center:	CMM
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Proprietor(s):	Fondazione Bruno Kessler (Bruno Kessler Foundation)
IP status:	Patent pending. *** Fondazione Bruno Kessler was a beneficiary in the project “FOREnsic evidence gathering autonomous seNSOR — FORENSOR”, Grant Agreement No. 653355, funded by the Research Executive Agency (REA) of the European Commission. The technology presented hereby stems from such project. Considering the obligation to formally notify REA before an intended grant of an exclusive licence over this technology, in line with GA Article 30.3, FBK provided such notification. REA is currently considering the notification and a reply is expected to be received in the forthcoming weeks. All the applicants are hereby informed that REA might not allow an exclusive license. On the other hand, should the above-mentioned granting of any exclusive licence be allowed, FBK shall fully observe any possible obligations assumed with REA. By doing so, FBK undertakes to make sure that the applicant third party, if and when executing an exclusive license agreement with FBK, shall comply too with the said duties and obligations that might be foreseen in REA's reply.
Patent family:	IT102017000123149 (A1) — 2019-04-30; WO2019087041 (A1) — 2019-05-09
Application(s):	Surveillance and monitoring applications
Keyword(s):	Image sensors, Energy-efficient vision sensors
Abstract:	A method for making the comparison between a continuous value analogue signal (S1) and a continuous value digital signal (S2), which provides the steps of: defining a ramp analogue signal (RAMP) having linearly variable value over time by periodically varying, for each time interval belonging to first predetermined time intervals (ck1), a first digital numerical value (cnt1) by a predetermined quantity and converting

it into the ramp analogue signal (RAMP); simultaneously comparing, at each of the first predetermined time intervals (ck1), the value of the ramp analogue signal (RAMP) with the analogue signal (S1) and the first digital numerical value (cnt1) with the digital signal (S2); verifying at which of the first predetermined time intervals (ck1) the value of the ramp analogue signal (RAMP) equals the analogue signal (S1) and at which of the predetermined time intervals (ck1) the first digital numerical value (cnt1) equals the digital signal (S2); defining the time window (WIND) between the first equality and the second equality; increasing a second digital numerical value (cnt2) within the time window (WIND) for each time interval belonging to second predetermined time intervals (ck2).